Midterm **(Part-1)** ***(CSE331L.7 – Asif Ahmed Neloy\_Summer’20)***

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*Time 20 minutes, Marks 15 (You need to answer all questions).*

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1. Explain **LEA** and **Offset [2]**

**LEA**

**LEA is the acronym for “Load Effeective Address”. It loads the address to the register from memory.**

**Offset**

**‘Offset’ is the 8086’s convention of saying addresses, in other words, memory locations.**

1. What is the difference between **Data Segment** and **.Data?[3]**

**You have to have ‘END’ directive in Data Segment and .Data doesn’t need ‘END’ directive.**

1. Explain the following - **ASSUME DS:DATA CS:CODE[5]**

**This directive assumes/declares ‘Data Segment’ as ‘DATA’ and ‘Code Segment’ as ‘CODE’**

1. Write down the Registers that are used for Memory Accessing with Example (Give example with all registers) **[5]**

**SI – Source Index Register**

**DI – Destination Index Register**

**BP – Base Pointer**

**SP – Stack Pointer**

**IP – Instruction Pointer Register**